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[PDF] PCM-5894 PCM-5892

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... System **memory**: 8MB to 128MB. Two 72-pin SIMM sockets onboard. ... **Serial**
ports: Three RS-**232** and one RS-**232/422/485 serial** ports. ...
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Patent 4870622: DRAM controller cache

... This feature is used for **burst mode** operation ... data transfers in the burst or **block**
mode because of ... and the controller always making four accesses to the **memory**. ...
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... und Uhrzeit 31 - Floppy Seek 185 - IDE HDD **Block Mode** 187 - Multi ... 181 Fast EDO Leadoff
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... **Block Mode**.- A high-speed data transfer in which the ... **Burst Mode**.- A high-speed data
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2 **Generating series and performance bounds for convolutional codes over burst-error channels**
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4 **Error Distribution and Diversity Performance of a Frequency-Differential PSK HF Modem**
Porter, G.;
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Mei-Tjing Huang; Spagna, F.; Blink, J.;
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Pages:326 - 327, 480

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8 Analysis of ignitron switching characteristics for burst mode operation of dual and off resonance high frequency power supplies
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